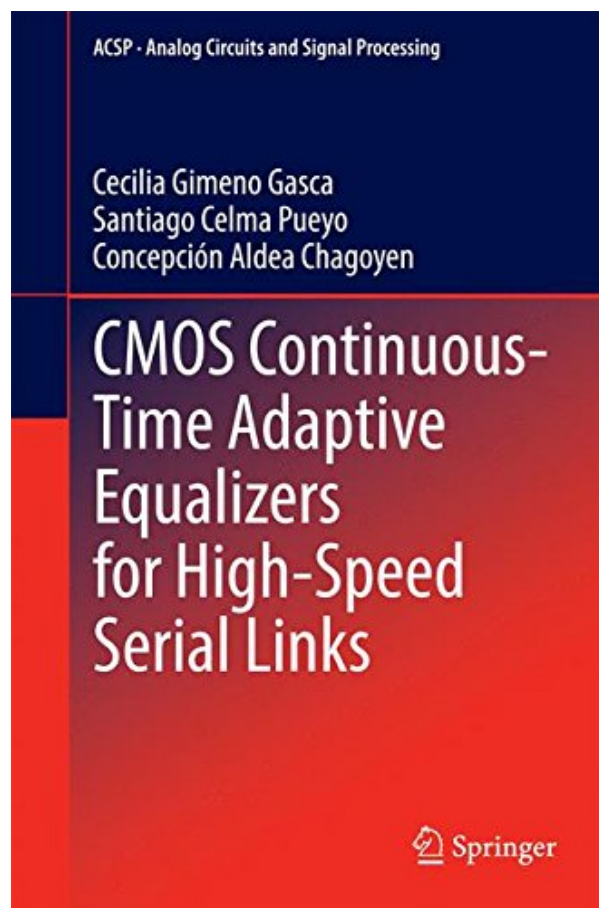


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- Covers complete design flow of continuous-time adaptive equalizers, from analysis of theoretical fundamentals to the final architecture;
- Includes analysis, design, and implementation of the main adaptive equalizer blocks, revealing key challenges and solutions in the design of such high performance cells;
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- Sales Rank: #2563673 in Books
- Published on: 2014-09-23
- Original language: English
- Number of items: 1
- Dimensions: 9.21" h x .44" w x 6.14" l, .0 pounds
- Binding: Hardcover
- 148 pages

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